

## AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method for reducing central processing unit (CPU) loading in a software receiver for a packet-based communications system, the method comprising the steps of:

measuring ~~the~~ a current CPU load by measuring an interrupt latency;

determining whether that the current CPU load ~~has exceeded~~ exceeds a predetermined threshold;

responsive to determining that the current CPU load ~~has~~ exceeded ~~[[a]]~~ the predetermined threshold, entering a power save mode by setting a Power Save (PS) bit in a frame control word, thereby signaling ~~the~~ a communications system transmitter to inhibit packet transmission and packet reception;

monitoring ~~the~~ CPU load while the communication system transmitter is inhibited;

determining that the monitored CPU load ~~has fallen~~ is below ~~[[a]]~~ the predetermined threshold; and

responsive to determining that the monitored CPU load is below the predetermined threshold, signaling the communications system transmitter to begin transmitting packets ~~once the CPU load has fallen below the predetermined threshold~~, wherein the signaling clears the PS bit.

2. (Currently Amended) The method of A method as in claim 1, wherein monitoring the CPU load is performed ~~the measurement of CPU loading is made~~ by an operating system background task.

3. (Canceled)

4. (Currently Amended) The method of A method as in claim 1, further comprising ~~wherein the transmitter signaling~~ the communication system transmitter to transmit a frame is performed during the power save mode.

5. (Currently Amended) The method of A method as in claim 1, in which the communications system is wireless.

6. (Currently Amended) The method of A method as in claim 1, in which the communications system is IEEE 802.11 wireless local area network (WLAN).

7. (Currently Amended) The method of A method as in claim 1, in which the communication system is Bluetooth.

8. (Currently Amended) The method of A method as in claim 1, in which the communications system is IEEE 802.15 wireless personal area network (PAN).

9-14. (Cancelled)

15. (Currently Amended) An apparatus, ~~for reducing CPU loading in a software receiver for a packet based communications system~~ comprising:

a processor configured with digital logic configured to:

measure ~~the~~ a current central processor unit (CPU) CPU load by measuring the response time of the CPU to a request for processor time;

determine ~~whether~~ that the current CPU load exceeds ~~has exceeded~~ a predetermined threshold;

responsive to determining that the current CPU load has exceeded ~~a~~ the predetermined threshold, enter a power save mode by setting a Power Save (PS) bit in a frame control word, thereby signaling ~~the~~ a communications system transmitter to inhibit packet transmission and packet reception;

monitor the CPU load while the communication system transmitter is inhibited;

determine ~~whether~~ that the monitored CPU load ~~has fallen~~ is below [[a]] the predetermined threshold; and

responsive to determining that the monitored CPU load is below the predetermined threshold, signal the communications system transmitter to begin

transmitting packets ~~once the CPU load has fallen below the predetermined threshold,~~  
wherein the signaling clears the PS bit.

16. (Currently Amended) The apparatus of claim 15, wherein monitoring the CPU load is performed by the measurement of CPU loading is a background task.

17. (Currently Amended) The apparatus of claim 15, wherein the current CPU load measurement is based on the response time of a host CPU to a request for interrupt.

18. (Currently Amended) The apparatus of claim 15, wherein the processor is further configured to signal the communications system transmitter to transmit a frame signaling is performed during the power save mode.

19. (Previously Presented) The apparatus of claim 15, wherein the communications system is wireless.

20. (Previously Presented) The apparatus of claim 15, wherein the communications system is at least one of: an IEEE 802.11 wireless local area network (WLAN); a Bluetooth system; and an IEEE 802.15 wireless personal area network (PAN).

21. (Currently Amended) A system, ~~for reducing CPU loading in a software receiver for a packet-based communications system comprising a processing means~~ comprising:

means for measuring ~~the a~~ current central processor unit (CPU) CPU load by measuring an interrupt latency;

means for determining ~~whether that~~ the current CPU load ~~has exceeded~~ exceeds a predetermined threshold;

means for, responsive to determining that the current CPU load ~~has exceeded~~ a the predetermined threshold, entering a power save mode by setting a Power Save (PS) bit in a frame control word, thereby signaling ~~the a~~ communications system transmitter to inhibit packet transmission and packet reception;

means for monitoring the CPU load while the communication system transmitter is inhibited;

means for determining that the monitored CPU load ~~has fallen~~ is below ~~[[a]]~~ the predetermined threshold; and

means ~~for, responsive to~~ determining that the monitored CPU load is below the predetermined threshold, signaling the communications system transmitter to begin transmitting packets ~~once the CPU load has fallen below the predetermined threshold~~, wherein the signaling clears the PS bit.

22. (Previously Presented) The system of claim 21, wherein the monitoring the CPU load is performed by measurement of CPU loading is made as a background task.

23. (Canceled)

24. (Currently Amended) The system of claim 21, ~~wherein the transmitter further comprising means for~~ signaling the communications system transmitter to transmit a frame is performed during the power save mode.

25. (Previously Presented) The system of claim 21, wherein the communications system is wireless.

26. (Previously Presented) The system of claim 21, wherein the communications system is at least one of: an IEEE 802.11 wireless local area network (WLAN); a Bluetooth system; and an IEEE 802.15 wireless personal area network (PAN).